

11 setting a width indication bit in the first stack save area in memory, and transferring the data values
12 from the registers to a second stack save area in memory and transferring the stack pointer value
13 from the stack pointer register to the second stack save area if the least significant bit of the stack
14 pointer register indicates the second word size such that the width indication bit in the first stack
15 save area in memory indicates that the data values for the procedure have the second word size].

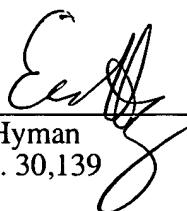
REMARKS

Entry of the foregoing amendment to claim 33 is requested. It is noted that the only issue raised in the outstanding Office Action is an amendment to the claims in compliance with 37 CFR 1.121(b). It is submitted that the amendment to Claim 33, previously presented and repeated in this amendment, as in compliance with 37 CFR 1.121(b).

Respectfully submitted,

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